AMENDMENTS TO THE SPECIFICATION:

On page 1, line 4 delete the following header:

Description

On page 1, line 5, in the space above the paragraph beginning "The present invention relates to..." please insert the header:

FIELD OF INVENTION

Replace the paragraph beginning on page 1, line 6 with the following new paragraph:

The present invention relates to an integrated semiconductor apparatus having trimmable generators.

Please add the following <u>new</u> paragraph after the paragraph ending at page 1, line 8:

PRIORITY OF OTHER APPLICATIONS

This application claims priority from German Patent Application No. 102 38 279.4-53, filed August 21, 2002, and entitled SHIFT REGISTER CHAIN FOR TRIMMING GENERATORS FOR AN INTEGRATED SEMICONDUCTOR APPARATUS.

On page 1, line 9, in the blank space above the paragraph beginning "Integrated semiconductor...", insert the header:

BACKGROUND

On page 2, line 12, in the blank space above the paragraph beginning "Against the background of ...", insert the following header:

SUMMARY

Delete the paragraph beginning on page 2, line 19, which begins "The object is achieved by...".

On page 6, line 31, insert the header:

BRIEF DESCRIPTION OF THE DRAWINGS

On page 6, line 36, in the blank space above the paragraph beginning "Figure 1 shows ...", insert the header:

DETAILED DESCRIPTION

Delete page 10 in its entirety.

Please delete the previous abstract at page 14 and add the following new abstract:

An integrated semiconductor apparatus includes at least one fuse box device that includes fuses for storing trimming data, a fuse box trimming output, and a timer emitting a clock signal. The apparatus also includes a parallel/serial converter that is connected to the fuses and to the timer. The parallel/serial converter is configured to read, in parallel, the trimming data from the fuses and to emit, in serial, the trimming data from the fuse block trimming output based on the clock signal. The apparatus also includes generators. Each generator generates a generator signal and includes a trimming unit that has a trimming signal input. The trimming unit is configured to trim the generator signal of the generator based on the trimming data received. The generator also includes a trimming signal output and memory flip-flops that connect the trimming input signal to the

trimming output signal. Each of the memory flip-flops connects each trimming unit to the fuse block trimming output to form a shift register chain for serial transmission of the trimming data from the fuse block device to the generators.